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RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITING PROCEDURE APR 10 2003
EXAMINING GROUP 2826
TECHNOLOGY CENTER 2800

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: VON AMMON ET AL 9

SERIAL NO.: 09/813,240 ✓ EXAMINER: J. MONDT

FILED: MARCH 20, 2001 GROUP: 2826

TITLE: SEMICONDUCTOR WAFER MADE FROM SILICON AND
METHOD FOR PRODUCING THE SEMICONDUCTOR WAFERS

RESPONSE TO FINAL OFFICE ACTION

ATTN: BOX AFTER FINAL
Assistant Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

This is in response to the Final Office Action dated December 12, 2002, with the time for reply having been extended for one month from MARCH 12, 2003, until APRIL 12, 2003.

REMARKS

At the outset, the Applicants wish to thank Primary Examiner Tran and Patent Examiner Mondt for the many courtesies extended to the undersigned attorney during the Personal Interview on March 18, 2003, at the U.S.P.T.O. The substance of the interview is set forth in the Examiner Interview Summary, and in this Response.